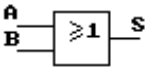
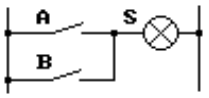
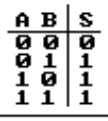
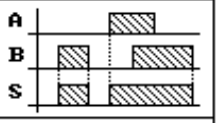
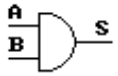
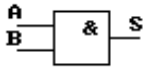
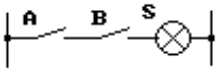
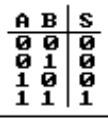
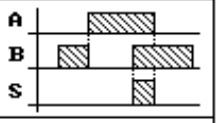
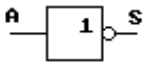
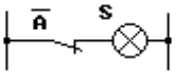
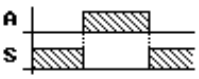
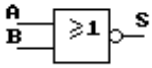
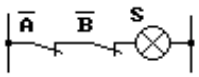
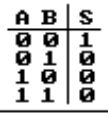
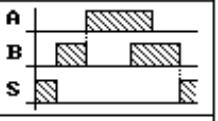
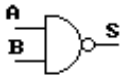
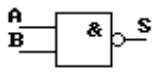
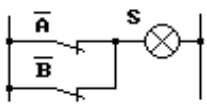
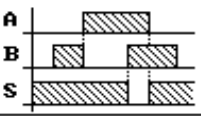
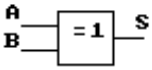
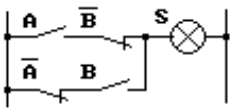
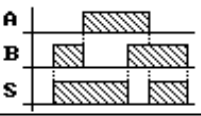
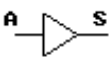
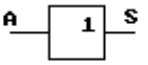
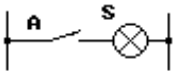
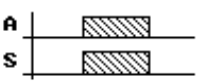
GIMNASIO SABIO CALDAS	**GIMNASIO SABIO CALDAS (IED)** **Nuestra escuela: una opción para la vida** **PLAN ESCOLAR NO PRESENCIAL**	Código	PENP - 01
		Versión	001
		Fecha	18/03/2020
		Proceso	Gestión Académica

DOCENTE	Sandra Milena Ramírez	**GRADO**	NOVENO
ASIGNATURA	Tecnología - Robótica		
Correo electrónico Contacto	sandra.ramirez@sabiocaldas.edu.co		
Fecha de envío	21 de Julio de 2020	**Fecha de entrega**	24 de Julio de 2020
Tiempo de ejecución de la actividad	2 horas		
TEMA	ELECTRÓNICA DIGITAL: Compuertas lógicas		

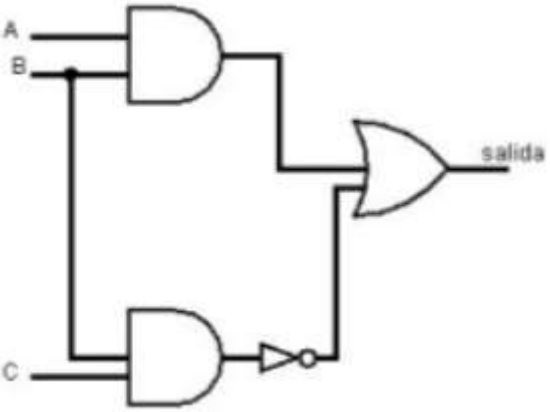
Contextualización

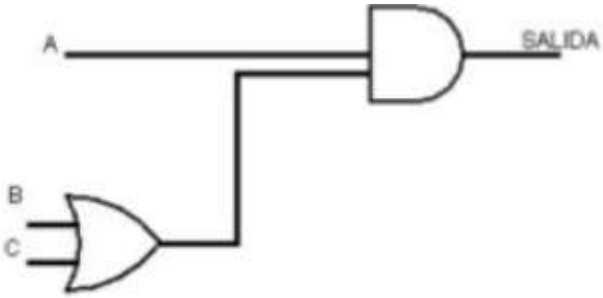
Función	Ecuación lógica	Símbolos			Tabla de verdad	Cronograma
		Norma MIL	Norma IEC	Circuito físico con contactos		
OR	$S = A+B$					
AND	$S = A \cdot B$					
NOT	$S = \overline{A}$					
NOR (OR+NOT)	$S = \overline{A+B}$ $S = \overline{A} \cdot \overline{B}$					
NAND (AND+NOT)	$S = \overline{A \cdot B}$ $S = \overline{A}+\overline{B}$					
EXOR	$S = A \oplus B =$ $= A\overline{B}+\overline{A}B$					
EXCITADOR	$S = A$					

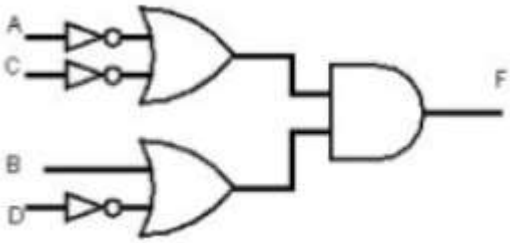
Descripción de la actividad sugerida

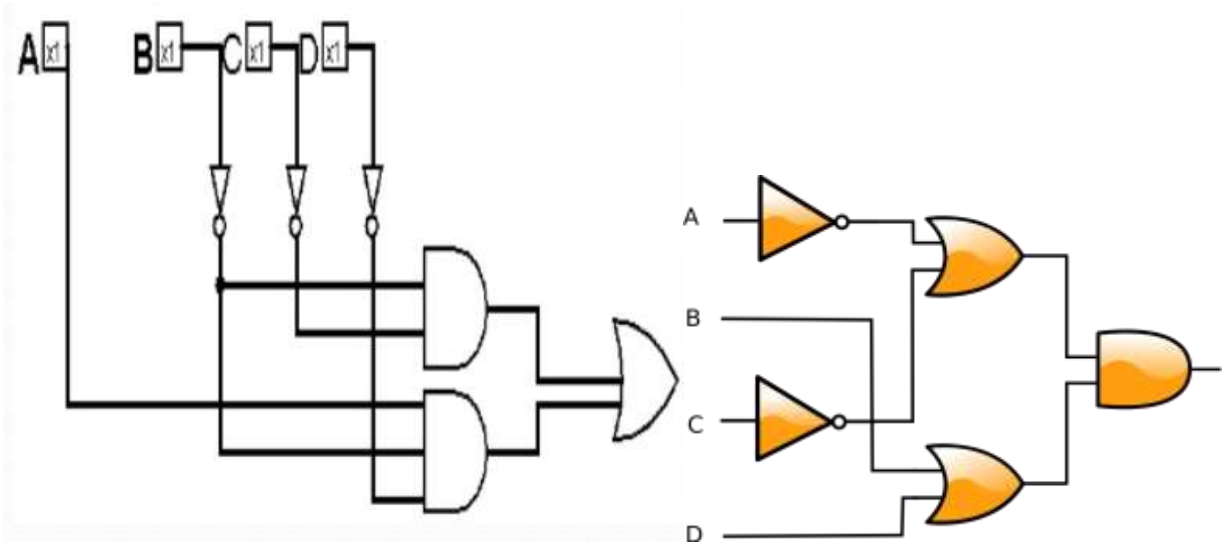
Tener en cuenta que la elaboración de esta guía se va a realizar en la tutoría virtual

Por medio del simulador logic.ly que se encuentra en línea se deben realizar los siguientes circuitos y probar sus tablas de verdad y sacar la fórmula correspondiente









Webgrafía/material fotocopiado (Anexo)
https://logic.ly/ https://sites.google.com/site/electronicadigitalmegatec/home/compuertas-logicas
Criterios de Evaluación
Realizar mediante simuladores esquemas de compuertas lógicas y probar su funcionamiento de manera correcta